

# **ARM926EJ-S S3C2450 Mango24R2 OM PIN Boot Mode**

<http://www.mangoboard.com/>

<http://cafe.naver.com/embeddedcrazyboys>

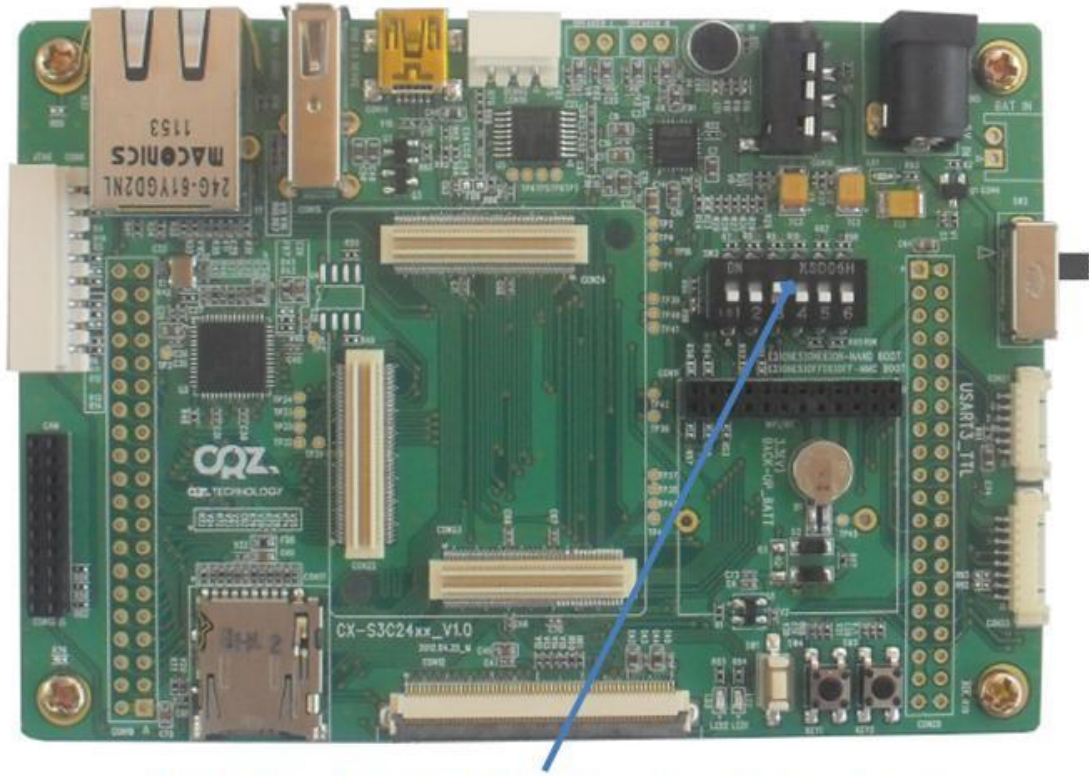
**Crazy Embedded Laboratory**

# Document History

Revision	Date	Change note

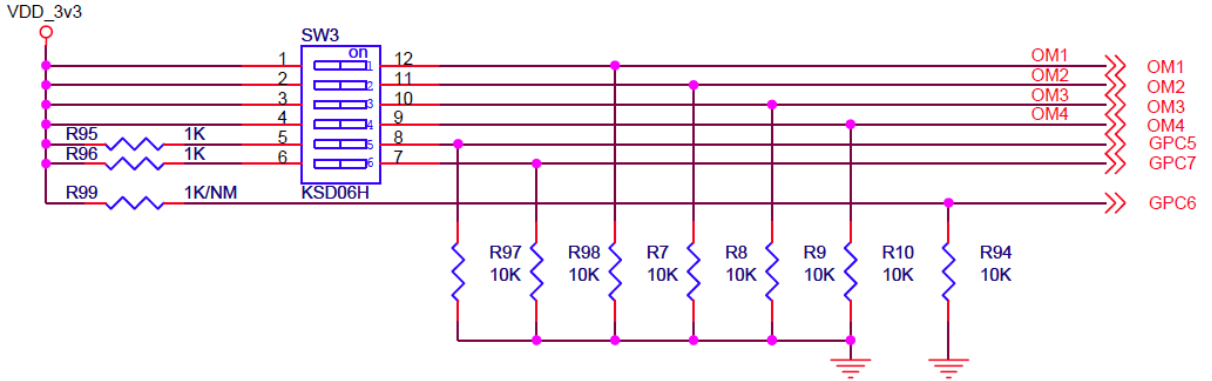
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# 1. Mango24R2 Boot Mode 설정



**[3] ON [5] ON [6] ON -> NAND BOOT**  
**[3] ON [5] OFF [6] OFF -> MMC BOOT**

# BOOT Option Switch



[3] ON [5] ON [6] ON -> NAND BOOT

[3] ON [5] OFF [6] OFF -> MMC BOOT

S3C2450\_um\_rev1.30\_20090305.pdf

을 참조하시면 됩니다. S3C2450 CPU Datasheet입니다.

## S3C2450 Operation Mode Description

Table 1-5. S3C2450 Operation Mode Description

OM[4]	OM[3]	OM[2]	OM[1]	OM[0]	OM[4]	OM[3]	OM[2]	OM[1]	OM[0]	Operation Mode
0	1	0	0	0	iROM				X-TAL	iROM
				1					EXTCLK	
1	0	0	0	0	Reserved					Reserved
				1	JTAG					JTAG
			1	0	0	OneNAND/ ROM	OneNAND (Muxed)	16-bit	X-TAL	OneNAND (Muxed)
					1				EXTCLK	
		1	1	0	0	0	ROM/ OneNAND (Demuxed)	8-bit	X-TAL	ROM/ OneNAND (Demuxed)
									1	
1	1	1	1	1		16-bit	X-TAL			
							EXTCLK			

5 GPC5/6/7 PIN CONFIGURATION TABLE IN IROM BOOT MODE

망고보드는 NAND Flas는 2048 Page에 5cycle 이므로

	Page	Address Cycle	GPC7 [2]	GPC6 [1]	GPC5 [0]
MMC(MoviNAND/iNand)	-	-	0	0	0
Reserved	-	-	0	0	1
Nand	512	3	0	1	0
		4	0	1	1
	2048	4	1	0	0
	5	1	0	1	
	4096	5	1	1	0